

## **KOS GALATSI, PHD MBA**

Los Angeles, California

Email : [kos@ee.ucla.edu](mailto:kos@ee.ucla.edu) Phone : (310) 874-3024

### **Experience**

- *Chief Operating Officer* of the Center for Functional Engineered Nano Architectonics (FENA) and the Western Institute of Nanoelectronics (WIN), UCLA, 2004 - present
- *Postdoctoral Fellow*, RMIT University, Melbourne, Australia, 2002-2003

### **Education**

- MBA, LaTrobe University, Melbourne, Australia, 2003-2004
- PhD, RMIT University, Melbourne, Australia, 1999 - 2002
- B.Eng, Hons, RMIT University, Melbourne, Australia, 1995 - 1998

### **Achievements**

- Published three chapters in the Encyclopedia of Sensors, Carbon Monoxide and CRC Handbook of Nanoscience, Engineering, and Technology
- Published 20 journal papers and 60 conference papers on matters ranging from memory devices to sensors
- 2006 Granted front cover of "IEEE Circuits and Devices Magazine" based on Emerging Memory Article published in May/June 2006
- 2004 Awarded Highest Exam Scorer in Financial Management, La Trobe University, MBA Class 2003
- 2004 Awarded International Travel Research Grant by The Australian Science Academy
- 1999 Awarded Best IEEE Graduate Paper in Australia by IEEE Australia Committee
- 1997 Australian Postgraduate Scholarship & Awarded Top-Up Scholarship by the Australian Collaborative Research Center on Microelectronics

### **Other**

- Book reviewer for Springer and contributing writer to Nanotechnology Now
- Member of the Institute of Electrical and Electronic Engineers (IEEE)
- Contributor to ITRS Chapters on emerging research materials, architecture & spintronics

### **Bio**

Kos Galatsis is the COO for two leading nanoelectronics industry Centers headquartered at UCLA which are widely known as the FENA Center ([www.fena.org](http://www.fena.org)) and the WIN center. ([www.win-nano.org](http://www.win-nano.org)). FENA explores beyond CMOS materials, devices and architectures and WIN seeks to develop new spintronic logic devices. In his role as COO, Galatsis manages and oversees 60 projects at 17 top ranked universities and is responsible in communicating research achievements to stakeholders such as the semiconductor industry. He leads overall project management, operations, execution and technical benchmarking efforts in areas such as memory devices, state variables, computer architectures and nanopatterning techniques. He works closely with research groups from Intel, IBM, AMD, MICRON and the SRC.

Prior to joining UCLA, Galatsis completed his MBA from LaTrobe University, Australia and PhD and B.Eng in Computer Systems from the Royal Melbourne Institute of Technology University (RMIT), Australia. During his studies he was awarded the "*Best Postgraduate Technical Paper in Australia*" by the IEEE Australia and achieved recognition for top exam scorer in Financial Management.